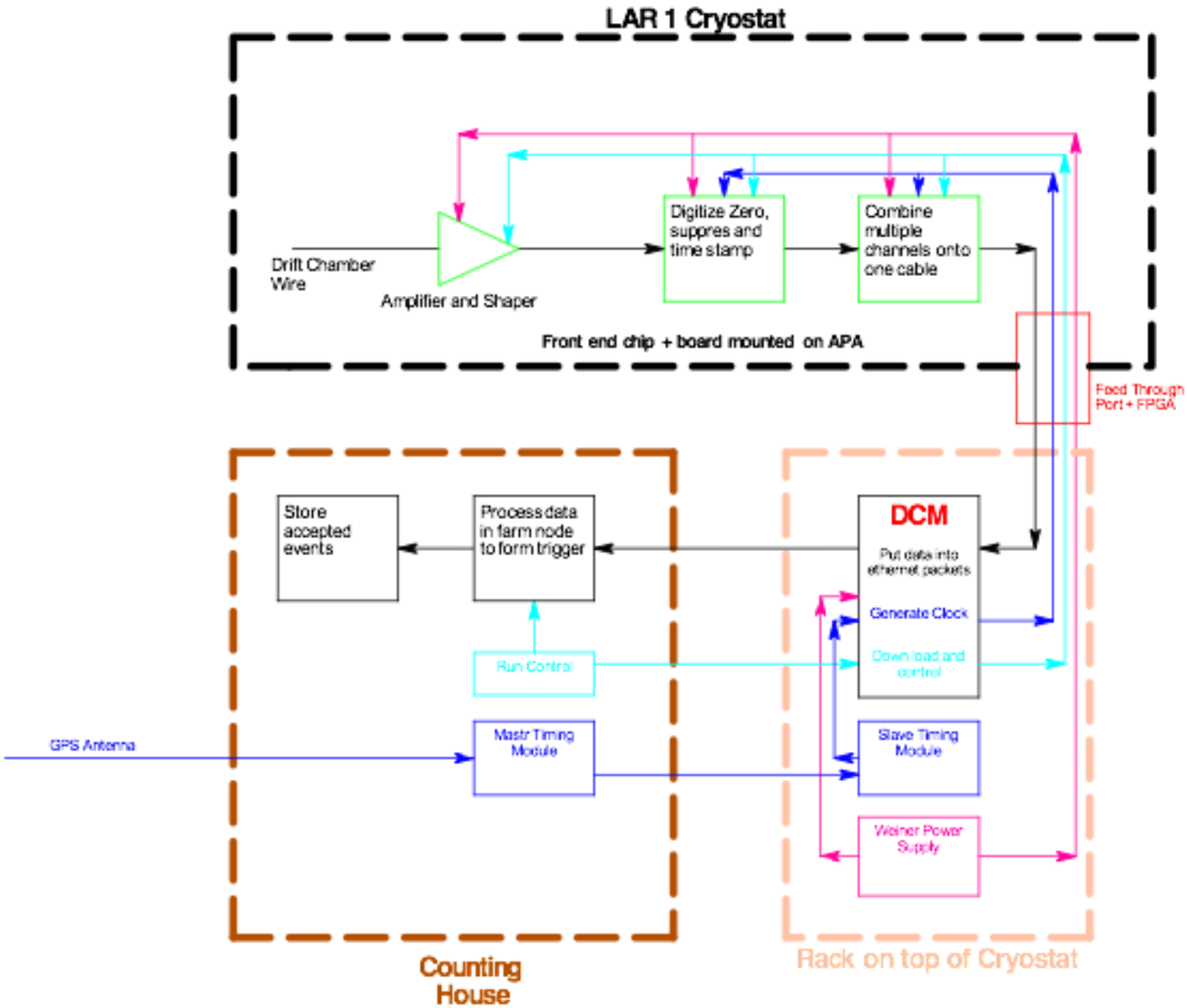
LAR 1 Cryostat
Drift Chamber Wire
Amplifier and Shaper
Digitize Zero, suppres and time stamp
Combine multiple channels onto one cable
Front end chip + board mounted on APA
Feed Through Port + FPGA
Store accepted events
Process data in farm node to form trigger
DCM
Put data into ethernet packets
Generate Clock
Down load and control
Run Control
GPS Antenna
Mastr Timing Module
Slave Timing Module
Wiener Power Supply
Counting House
Rack on top of Cryostat